

VMETRO Inc.

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www.vmetro.com**Phoenix VPF1**

The Phoenix VPF1 is a 6U VME64x data and signal processor supporting VITA 41 backplane switch fabric communications via VXS over P0. The VPF1 comprises four processor nodes: two 1 GHz PowerPC 7447A CPUs and two Xilinx XC2VP70 Virtex-II Pros. Each processor has fully distributed memory, and each FPGA supports multiple inter-node communication channels via RocketIO. These channels bind together local processors and those on separate boards for seamless and scalable processing.

A PMC site, GbE, and P2 resources enable I/O. A comprehensive suite of PowerPC and FPGA tools facilitate rapid development and deployment. Phoenix VPF1s are available in air-cooled or conduction-cooled builds, enabling easy migration from development to production.

**FEATURES:**

- 2x PowerPC 7447 CPU nodes and 2x Xilinx Virtex-II Pro FPGA nodes
- 8x 2.0-3.125 Gbps off-board serial communications channels
- Ethernet, RS-232, RS-422
- 64-bit, 66 MHz PMC site for local I/O
- VXS (VITA 41) compatible
- Air-cooled and rugged, conduction-cooled build variants