

Xilinx, Inc.

2100 Logic Drive • San Jose, CA 95124

408-559-7778

www.xilinx.com**Xilinx Connectivity Solutions**

Xilinx provides comprehensive serial and parallel connectivity solutions that meet the most demanding bandwidth, power, and cost targets for chip-to-chip, backplane, and box-to-box applications. Design for success with Virtex®-6 and Spartan®-6 FPGAs to leverage the lowest power, highest-speed serial transceivers, and widest protocol support, including PCIe, GbE, XAUI, SONET/SDH, CPRI, OBSAI, Serial RapidIO, and HD-SDI. Built-in PCIe and Tri-Mode Ethernet MAC blocks save valuable logic resources and power, while enabling high performance connectivity at lower cost.

Xilinx Targeted Design Platforms offer off-the-shelf, protocol-based solutions with IP cores, tools, reference designs, and development kits to get designs up and running out-of-the-box. These include 5 Gbps GTX transceivers compliant with PCIe Version 2.0, the lowest power and highest performance programmable PCIe integrated blocks, and the only 100 GbE MAC integrated block delivered in a single FPGA.

For more information, contact: lisa.hartman@xilinx.com**FEATURES**

- › **Virtex-5 FPGA Development Kit for PCI Express®:** Create high-performance 1-8 lane PCIe® systems for communications and networking, video and broadcast, storage and computing, industrial, and aerospace/defense applications
- › **Spartan-3 FPGA PCI Express® Starter Kit:** Low-risk path for high-volume applications provides a two-chip solution pairing Spartan-3 FPGA and NXP PHY with an embedded single-lane PCI Express endpoint that is fully compliant with the PCIe v1.1 specification
- › **Virtex-5 FPGA Gigabit Ethernet Development Kit:** Scalable, on-chip Ethernet connectivity at 10/100 Mbps and 1 Gbps speeds with four Tri-Mode Ethernet MACs and embedded low-power serial transceivers to support MII, GMII, RGMII, and SGMII physical layer (PHY) interfaces

RSC# 42299 @ www.embedded-computing.com/rsc