

Annapolis Micro Systems, Inc.

190 Admiral Cochrane Drive, Suite 130 • Annapolis, MD 21401

Tel: 410-841-2514 • Fax: 410-841-2518

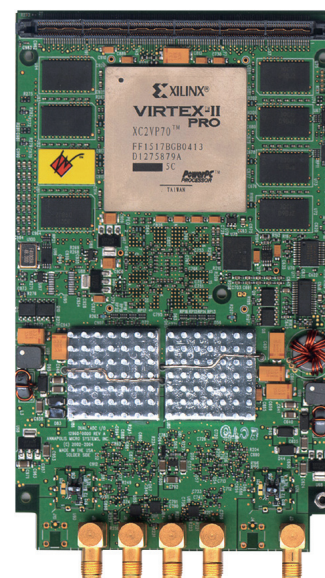
www.annapmicro.com**Dual 1.5 GHz A/D Board**

Annapolis Micro Systems is a world leader in high-performance COTS FPGA-based processing for radar, sonar, SIGINT, ELINT, digital signal processing, FFTs, communications, software radio, encryption, image processing, prototyping, text processing, and other processing intensive applications.

The Annapolis Dual 1.5 GHz A/D I/O card provides two channels of 1.5 GSps input (with 8-bit resolution) or one channel of 3.0 GSps input (with 8-bit resolution). The board has two Max108 8-bit ADCs, each one fed by its own pair of differential signals. The differential clock signals coming into the board can provide identical clocks to both A/Ds for dual 1.5 GSps channels or an inverted clock to one of the A/Ds to interleave the data for a single 3.0 GSps channel. Multiple I/O cards can be synched together via the Annapolis Clock Sync Distribution Board, as in the 24 GSps A/D Collection, Processing, and Distribution (CPD) system.

The Xilinx Virtex-II Pro 70 on the board provides user-configurable, real-time continuous sustained processing of the full data stream. Up to two of these I/O cards can reside on the Annapolis WILDSTAR II or WILDSTAR II Pro FPGA-based VME and PCI bus boards, which provide up to 30 million more user reprogrammable FPGA gates for onboard processing. Our boards run on many different operating systems. We support our board products with a standardized set of drivers, APIs, and VHDL simulation models. VHDL source is provided for interfaces to SRAM, LD bus, I/O bus, and DACs. CoreFire users will have the usual CoreFire board support package.

Annapolis is famous for the high quality of our products and for our unparalleled dedication to ensuring that the customer's applications succeed. We offer training and exceptional special application development support, as well as more conventional customer support.

**FEATURES:**

- Two MAX 108 8-bit A/D converters – two 1.5 GSps channels or one 3 GSps channel
- Two sets of differential input signals – one for each A/D
- Differential clock input – Synchronize or interleave the two data streams
- Up to 6 Gbps I/O bandwidth to WILDSTAR II Pro motherboard I/O slot
- Xilinx Virtex-II Pro XC2V70 FPGA for user reprogrammable processing
- Heat sink on FPGA
- Host software: Win NT, 2000, XP, Linux, VxWorks, Solaris, MacOS, DECAAlpha, and SGI
- Full CoreFire board support package for fast and easy application development
- VHDL model, including some source code
- Save time and effort and reduce risk with COTS boards and software
- Achieve world-class performance – WILD solutions outperform the competition
- Includes one-year hardware warranty, software updates, and customer support; training available

■ For more information, contact: wfinfo@annapmicro.com

RSC# 16202 @ www.mil-embedded.com/rsc