

Avnet Memec

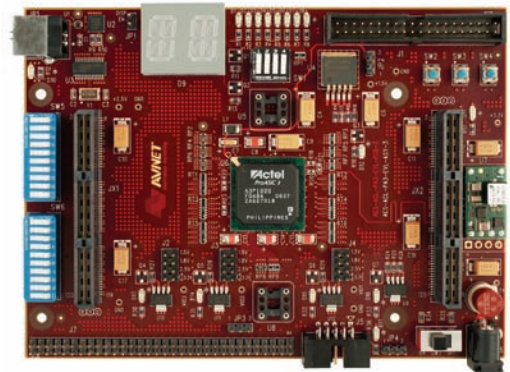
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Design/IP Support

Avnet Memec helps specify, develop, and design FPGAs by serving as an extension of your design team. Avnet Memec provides the additional resources and knowledge base during the critical implementation phase, so you can meet aggressive time-to-market milestones. Avnet Memec offers a wide set of intellectual property (IP) cores to assist your design.

Avnet Memec has locations across North America and features FPGAs from Actel. Avnet Memec's factory trained and certified field applications engineers are readily available to support all of your design requirements.



FEATURES:

- › Core Integration: Avnet Memec has developed high-level building blocks that can be easily adapted to new designs
- › Design Optimization: Avnet Memec can optimize existing designs, improving performance, density, power consumption, and reliability
- › Technology Migration: Avnet Memec can take older technology designs and target to newer devices, saving money and improving availability
- › Complete System Design: Avnet Memec can do complete system designs, adding support and interface devices to implement an entire application
- › C Code Conversion: Utilizing Celoxica's DK1 Design Suite, Avnet Memec can convert C code to an EDIF netlist or RTL VHDL