



Innovative Integration

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X5-400M

The X5-400M is an XMC I/O module featuring two 14-bit, 400 MSPS A/D and DAC channels with a Virtex5 FPGA computing core and PCI Express host interface on a standard XMC module. An Xilinx Virtex5 LX110T (SX95T when available) with 1 GB DDR2 DRAM and 4 MB QDR-II memory provide a very high performance DSP core for demanding applications such as emerging wireless standards. The close integration of the analog IO, memory and host interface with the FPGA enables real-time signal processing at extremely high rates exceeding 300 GMACs per second. The X5 XMC modules couple Innovative's powerful Velocia architecture with a high performance, 8-lane PCI Express interface that provides over 1 GB/s sustained transfer rates to the host. Get Pricing and Data Sheets online now.

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Integration**
... real time solutions!



FEATURES

- ✦ Two 400 MSPS, 14-bit A/D channels · Two 400 MSPS, 14-bit DAC channels
- ✦ ±1V, 50 ohm, SMA inputs and outputs · Xilinx Virtex5, LX110T FPGA (SX95T coming) 1 GB DDR2 DRAM
- ✦ 4 MB QDR-II SRAM8 Rocket IO private links, 2.5 Gbps each >1 GB/s, 8-lane PCI Express Host Interface
- ✦ Power Management features · XMC Module (75 mm x150 mm) PCI Express (VITA 42.3)
- ✦ Applications include: · Wireless Receiver and Transmitter · WLAN, WCDMA, WiMAX front end RADAR
- ✦ Electronic Warfare · High Speed Data Recording & Playback · High speed servo controls · IP development

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