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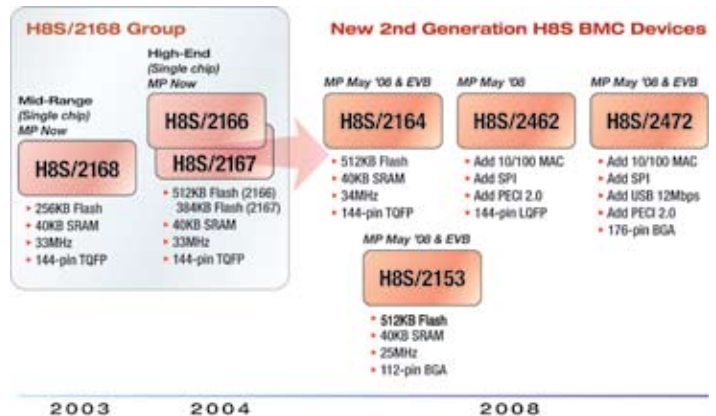
www.renesas.com**H8S/2100 BMCs**

Renesas has the world's broadest range of board management controllers for AdvancedTCA, AMC, and μ TCA applications, including IPMCs, MMCs, MCMCs, and EMMCs. For the best solutions for new designs, check out our expanding selection of 16-bit devices with LPC in the H8S/2100 and H8S/2400 series, some of which include PECEI, Ethernet, USB, SPI, and 16550 functions.

Consider the new H8S/2153, for example. This space saving (10 mm x10 mm, 112-pin BGA) second-generation microcomputer has a 25MHz H8S/2600 CPU, 40KB of SRAM, 512KB of flash, 4 channels of I2C, and LPC, plus other key peripherals with field-proven functional modules from the H8S/2168 family. The H8S/2153's software compatibility makes it easy to port firmware from our first-generation solutions; its SRAM memory map is identical, too.

For more information on these or other Renesas products, contact webmaster.america@renesas.com.

Or visit our website: www.renesas.com.

**H8S BMC Feature Overview**

BMC Feature	H8S/2168, 67, 66	H8S/2153	H8S/2164	H8S/2462	H8S/2472
Package - Pins	TQFP-144	BGA-112	TQFP-144	LQFP-144	BGA-176
Clock Freq	33MHz	25MHz	34MHz	34MHz	34MHz
CPU	H8S/2000	H8S/2600	H8S/2600	H8S/2600	H8S/2600
On-chip SRAM Size	40KB	40KB	40KB	40KB	40KB
Flash Size	256K, 384K, 512K	512KB	512K	512K	512K
User Boot Flash Size	8KB	16KB	16KB	16KB	16KB
I2C channels	6	4	6	6	6
External bus width (for SRAM)	16-bit	None	16-bit	16-bit	16-bit
Glueless External Bus I/F	No	-	Selectable	Selectable	Selectable
16550 with Snoop	None	None	Yes	Yes	Yes
SCI serial	3 ch @ 115.2Kb	2 ch @ 115.2Kb	2 ch @ 115.2Kb	2 ch @ 115.2Kb	2 ch @ 115.2Kb
Total Serial Channels Available	3 ch	2 ch	3 ch	3 ch	3 ch
SPI	None	None	None	Yes	Yes
10/100 Ethernet MAC	None	None	None	1 ch	1 ch
USB Full-speed Function Interface	None	None	None	None	Yes-EP0-EP3
PECEI 2.0 Support	None	None	None	Yes	Yes
H/W Multiplier	None	Yes	Yes	Yes	Yes
14-bit PWM	4 ch	4 ch	4 ch	4 ch	4 ch
LPC	3 ch	3 ch	3 ch	3 ch	3 ch
E10A-USB Debugger	Yes	Yes	Yes	Yes	Yes