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**ISE Design Suite 10.1**

The ISE® Design Suite 10.1, the latest release of the industry-leading design tools from Xilinx, delivers the perfect combination of design performance and productivity. Whether your design requires a flexible embedded processing solution, a specialized flow for DSP development, or just optimal high-performance logic, the ISE Design Suite 10.1 can be configured to help you achieve your design goals quickly.

Integrated Environment for Embedded Design

The Embedded Development Kit (EDK) bundle is an integrated software solution for designing embedded processing systems. This pre-configured kit includes the award winning Platform Studio tool suite as well as all the documentation and IP that you require for designing Xilinx Platform FPGAs with embedded PowerPC® hard processor and/or MicroBlaze™ soft processor cores.

Integrated Environment for DSP Design

The ISE Design Suite 10.1 includes the XtremeDSP™ Development Tools Package as an option. Developers with little FPGA design experience can quickly create production quality FPGA implementations of DSP algorithms in a fraction of traditional RTL development times.

**FEATURES**

- › Meet your Design Goals Faster:
 - One integrated front-to-back FPGA IP catalog and design tool suite with unified interoperability
 - Domain specific design capture for DSP, embedded and logical design
 - Accelerated system development via customization and integrated libraries of optimized IP
- › Achieve Optimal System Performance:
 - Design tools optimized to minimize area while maximizing performance for Virtex-5 and Spartan-3 family Platform FPGAs
 - Designer control to streamline every component of your application, throughout the development process
 - An integrated design environment delivering performance without compromising power
- › Access World Class FPGA Design Solutions
 - Logic design tools delivering optimal timing closure for higher performance, lower power designs
 - Automated embedded design wizards to accelerate processing development and time-to-market
 - DSP design flows and IP tailored for algorithm, system, and hardware developer