## Processing and logic: FPGAs

## Xilinx, Inc.

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The Xilinx Spartan®-6 FPGA family offers an optimal balance of cost, power, and performance for consumer, automotive, surveillance, wireless, and cost/power-sensitive applications. Built for connectivity and low power, Spartan-6 FPGAs provide the programmable foundation for Xilinx Targeted Design Platforms that accelerate innovation and improve differentiation of next-generation electronics products.

System developers can meet the demand for new features, while reducing system costs by up to half for lower-power "greener" products. A rich selection of built-in system-level blocks — including DSP slices, high-speed transceivers, and PCI Express® interface cores — enable greater system-level integration. Spartan-6 FPGAs bring 65 percent lower power than previous Spartan families with innovations in advanced power management technology and a lower-power 1.0 V core option. In addition, fast, flexible I/O deliver over 12 Gbps memory access bandwidth with 3.3 V compatibility and RoHS-compliant Pb-free packaging.





## **FEATURES**

- Spartan-6 LX FPGAs optimized for absolute lowest cost with up to 150K logic density, 4.8 Mb memory, integrated memory controllers, and easy-to-use, high-performance system IP including DSP blocks
- Spartan-6 LXT FPGAs optimized for lowest risk and lowest cost serial connectivity solution with up to eight 3.125 Gbps GTP transceivers and embedded PCI Express-compatible core
- Performance and flexibility to meet changing consumer requirements of automotive infotainment applications
- > Cost-effective alternative to ASICs for rapid development and delivery of flat panel displays within ever-shrinking product life cycles
- Ideal for video surveillance with standard- and high-definition resolution image processing, video analytics, and multi-channel video encoding for fast, efficient transmission

For more information, contact: more info@xilinx.com

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