

Innovative Integration

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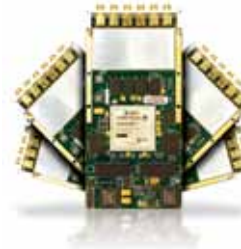
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PCI Express X5 & X3 XMC Product Family

The X3 and X5 module families feature high-performance analog and digital I/O on a standard PMC module format with a flexible PCI Express host interface bus. Each one has a powerful user-programmable FPGA computing core that is used for data acquisition and connects the I/O to the PCI Express host interface.

The X3 group provides an extremely cost-effective combination of analog or digital I/O plus a user-programmable FPGA plug-and-play, yet customizable solution for common data acquisition, waveform generation, and servo/control applications.

The X5 module family provides a state-of-the-art combination of the newest generation, high-density, user-reprogrammable Virtex-5 FPGA logic meticulously integrated with ultra-high-speed analog or digital I/O to address the most demanding radar, wireless RF, and communications applications. Customize COTS X5 Modules for your application.



FEATURES

- › Virtex-5 FPGA – SXT or LXT
- › QDR SRAM and DDR2 DRAM memory pools
- › PCI Express, 8 lane interface
- › Analog and digital I/O integrated with FPGA
- › >1 GB/s dedicated secondary host interface
- › Compact IEEE 1386 card format (75x150mm)
- › Integrate into any VITA 42.3 PCI Express system
- › >30 GMACs/s (SX95T) integrated with memory blocks and logic
- › Real-time memory performance to 4 GB/s for FPGA data buffering and computation
- › >1 GB/s transfer rates to host eliminates custom hardware requirements